



PATENT
P54428RE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: SEUNG-CHEOL HONG *et al.*

Original Patent No. 5,944,830 issued on 31 August 1999

Serial No.: 09/942,961

Examiner: MYERS, PAUL R.

Filed: 31 August 2001

Art Unit: 2112

For: REDUCING POWER CONSUMPTION IN MONITOR BY SWITCHING OFF
HEATER POWER IN POWER-OFF MODE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Sir:

In response to Paper No. 29 dated 27 October 2004, following is submitted.

In accordance with 37 C.F.R. §1.56, and §§1.97 and 1.98 as amended, Applicant cites, describes and provide photocopies of the following art references. It should be noted that, per paragraph 4 on page 2 of Paper No. 29, the fee and certification requirements under 37 C.F.R. §1.97 were waived for the foregoing Information Disclosure Statement and PTO-1449.

1. U.S. Patent No. 5,389,952 to Kikinis, entitled *LOW-POWER-CONSUMPTION MONITOR STANDBY SYSTEM*, issued on February 14, 1995.
2. U.S. Patent No. 5,736,873 to Hwang, entitled *POWER SAVING CONTROL CIRCUIT FOR A DISPLAY APPARATUS*, issued on April 7, 1998.
3. Printout from Wikipedia, the free encyclopedia, entitled "*VESA Display Power Management Signaling*", 1 page.

4. Printout from VESA: Video Electronics Standards Association, *VESA- Standards: What Are They?*, pp 1-9.
5. Printout from The Free Dictionary com by Farlex, entitled *VESA Display Power Management Signaling*”, pp. 1-2.
6. *VESA Advanced Feature Connector (VAFC) Software Interface Standard*, by Video Electronics Standard Association, Milpitas, CA 95035, Version 1.0, Revision date March 30, 1994, 1 page.
7. *VESA Advanced Feature Connector (VAFC) Software Interface Standard*, by Video Electronics Standard Association, Milpitas, CA 95035, Version 1.1, Revision date November 30, 1995, 1 page.
8. Printout from VESA: Video Electronics Standards Association, *VESA- Standards: Summaries*, pp 1-4.
9. *M1 Display Interface System*, by Video Electronics Standard Association, Milpitas, CA 95035, Version 1.0, August 16, 2001 1 page.
10. *VESA BIOS Extensions/Power Management, (VBE/PM), Standard*, by Video Electronics Standard Association, Milpitas, CA 95035, Version 1.0, Approved: February 4, 1994, 1 page.
11. Printout from www.lextron.com, *HP AND ELONEX ANNOUNCE PATENT LICENSING AGREEMENT*, Elonex, Hewlett Packard, 18 November 2004, 2 pages.
12. *The Evolution of the Green PC: Towards Integrated Power Management*, by Gary Smerdon, Advanced Micro Devices, Network Products Division, Sunnyvale, CA, 1 page.

Kikinis '952 discloses a circuit for lowering power to a video display, that depends upon the synchronization signals.

Hwang '873 discloses a power control circuit of a monitor capable of being applied to all kinds of monitors.

The *VESA Display Power Management Signaling* describes the synchronisation signal scheme for reduction of power to a monitor.

The printout from VESA, *VESA- Standards: What Are They?*, describes the several standards published by VESA.

The printout from The Free Dictionary Com describes the synchronization signal/power reduction scheme of VESA.

VESA Advanced Feature Connector Version 1.0 describes the standardization of the open software interface.

VESA Advanced Feature Connector Version 1.1 describes the first revision of the open hardware interface standard.

The printout from VESA, *VESA- Standards: Summaries*, describes the standards issued through the 29th of October 2004.

MI Display Interface System describes a standard connector used in analog, digital or dual interface displays.

The *VESA BIOS Extensions/Power Management* paper mentions a hardware mechanism for controlling the power stage of display devices.

The letter from Elonex, Hewlett Packard is a press release announcing the licensing of the Kikinis '952 patent.

The Evolution of the Green PC: Towards Integrated Power Management is a background discussion of the display power management signal in standard.

The citation of the foregoing references is not intended to constitute an assertion that other or more relevant art does not exist. Accordingly, the Examiner is requested to make a wide-ranging and thorough search of the relevant art.

No fee is incurred by this Statement.

Respectfully submitted,

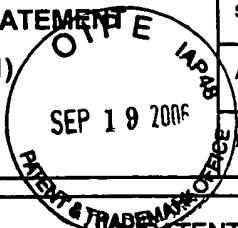


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INFORMATION DISCLOSURE STATEMENT		SERIAL NUMBER 09/942,961	DOCKET NO. P54427RE
PTO-1449 (PAGE 1 OF 1)		APPLICANT SEUNG-CHEOL HONG <i>et al.</i>	
		FILING DATE -31 August 2001	GROUP 2112



U.S. PATENT DOCUMENTS						
EXAMINER	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,389,952	2/14/95	Kikinis			
	5,736,873	4/07/98	Hwang			

FOREIGN PATENT DOCUMENTS					TRANSLATION		
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	Printout from Wikipedia, the free encyclopedia, entitled "VESA Display Power Management Signaling", 1 page
	Printout from VESA:: Video Electronics Standards Association, <i>VESA- Standards: What Are They?</i> , pp 1-9.
	Printout from The Free Dictionary.com by Farlex, entitled <i>VESA Display Power Management Signaling</i> , pp. 1-2.
	<i>VESA Advanced Feature Connector (VAFC) Software Interface Standard</i> , by Video Electronics Standard Association, Milpitas, CA 95035, Version 1.0, Revision date March 30, 1994, 1 page.
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EXAMINER: _____ DATE CONSIDERED: _____

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.